

What Is Claimed Is:

1. A method of driving a liquid crystal display panel using a 2-dot inversion system, the method comprising the steps of:

sequentially pre-charging a plurality of pixel cells of the liquid crystal display panel along a plurality of gate lines; and

sequentially charging the pixel cells with a plurality of data signals along the gate lines after pre-charging the pixel cells.

2. The method as claimed in claim 1, wherein the pre-charging step is carried out when the pixel cells on the preceding gate line charges the data signal.

3. The method as claimed in claim 1, wherein the pre-charging step is carried out at the time interval the same as the data signal charging step.

4. A method of driving a liquid crystal display panel

using a 2-dot inversion system, the method comprising the steps of:

applying signals having a polarity inverted every two gate lines to a plurality of source lines on the liquid crystal display panel; and

applying a plurality of gate signals having a width of two horizontal synchronization intervals and overlapping each horizontal synchronization interval to each gate line on the liquid crystal display panel.

5. A method of driving a liquid crystal display panel using a 2-dot inversion system, the method comprising the steps of:

allowing a plurality of pixel cells arranged on the liquid crystal display panel to cross a plurality of source lines and gate lines each other to charge a voltage stored in the pixel cell on the preceding gate line and a data signal on the source line; and

allowing the plurality of pixel cells to charge the data signal on the source line.

6. An apparatus for driving a liquid crystal display

9. An apparatus for driving a liquid crystal display panel employing a 2-dot inversion system, comprising:

a liquid crystal panel having a plurality of pixel cells arranged to cross a plurality of source lines and gate lines each other;

a data driver applying a data signal to each source line on the liquid crystal display panel to have a polarity inverted every two gate lines; and

a gate driver applying first gate signals having a width of two horizontal synchronization interval and overlapping each horizontal synchronization interval to the gate lines on the liquid crystal display panel.

10. The apparatus as claimed in claim 9, wherein the gate driver includes:

a gate driving integrated circuit chip applying a plurality of first gate signals to the gate lines, wherein the first gate signals have a width of one horizontal synchronization interval and sequentially enabled therein; and

a width controller connected to the gate driving integrated circuit chip and the gate lines and executing a

logical operation of each of the first gate signals and each of second gate signals to be applied to the preceding gate line, thereby generating the first gate signals.